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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete If Known

Application Number

Filing Date

First Named Inventor Alter, Martin

Art Unit

Examiner Name

Attorney Docket Number M085

Sheet

1

of

2**U.S. PATENT DOCUMENTS**

Examiner Initials ¹	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
<u>ME</u>	—	US-6489881	12-03-2002	Aleksandravicius	—
<u>ME</u>	—	US-5264723	11-23-1993	Strauss	—
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FOREIGN PATENT DOCUMENTS

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
RE	—	GARROU, Wafer-Level Packaging Has Arrived, Semiconductor International, Oct. 2000, 119 et seq	
RE	—	YOUNG, Wafer-level Processing, May 1997, Chip Scale Review, May 1997, 28 et seq.	
RE	—	NGUYEN et al., Assembly Considerations for SMD Wafer-Level CSPs, Chip Scale Review, May/June 2000, 48 et seq.	
RE	—	CHAN et al., Integrated Antennas on Si,..., IEDM 10-903, 0-7803-7050-3/01/\$10.00 copr. 2001 IEEE, 40.6.1 et seq.	
RE	—	PENNWELL CORP., Is THIS the industry's next killer app?, http://sst.pennnet.com/Articles/ , ... 7/14/2003, 7pp.	
RE	—	LIHUI et al., High Q Multilayer Spiral Inductor on Silicon Chip for 5-6 GHz, IEEE Electron Device Letters, Vol. 23, No. 8, August 2002, 470 et seq.	
<div style="position: absolute; top: 0; right: 0; width: 100%; height: 100%; border-top: 1px solid black; border-right: 1px solid black; transform: rotate(45deg); transform-origin: bottom right;"></div>			

Examiner Signature <i>Nehi Parrell</i>	Date Considered <i>07/20/04</i>
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